

WHAT IS CLAIMED IS:

*Subst. a1*

1. A display device comprising:  
an insulating substrate on which display electrodes and wiring electrodes are formed;  
an opposing substrate opposed to said insulating substrate; and  
a display material contained in a spacing between said insulating substrate and said opposing substrate in a sealing manner,  
wherein said wiring electrodes are metallized by plating, and each of portions of said wiring electrodes extending across a contour line of said opposing substrate is formed perpendicular to the contour line of said opposing substrate.

2. A display device comprising:  
an insulating substrate on which display electrodes and wiring electrodes are formed;  
an opposing substrate opposed to said insulating substrate; and  
a display material contained in a spacing between said insulating substrate and said opposing substrate in a sealing manner,  
wherein said wiring electrodes are metallized by plating, and a dummy electrode is provided outside the outermost one of said wiring electrodes.

3. A display device according to Claim 2, wherein the spacing between said dummy electrode and the outermost wiring

electrode is equal to or smaller than the spacing between the outermost wiring electrode and the inner wiring electrode next to the outermost wiring electrode.

4. A display device according to Claim 2, wherein said wiring electrodes comprise a plurality of wiring electrode groups each formed of a plurality of wiring electrodes, and dummy electrodes are provided outside the opposite-end wiring electrodes in each of said wiring electrode groups.

5. A display device according to Claim 2, wherein said dummy electrode is formed parallel to the outermost wiring electrode.

6. A display device comprising:  
an insulating substrate on which display electrodes are formed;

an opposing substrate opposed to said insulating substrate;

a display material contained in a spacing between said insulating substrate and said opposing substrate in a sealing manner;

a semiconductor chip for supplying signals to said display electrodes;

a group of pads arranged on said insulating substrate so as to correspond to connection terminals of said semiconductor chip;

a group of check pads provided between said display

electrodes and said group of pads; and

wiring electrodes for establishing electrical connections between said display electrodes and said group of check pads corresponding to the semiconductor chip terminals, and electrical connections between said group of check pads and said group of pads corresponding to the semiconductor chip terminals,

wherein said group of check pads are arranged in a straight row.

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7. A display device according to Claim 6, wherein some of said group of pads corresponding to the semiconductor chip terminals form a part of said group of check pads.

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